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PRESS RELEASE

DESIGNADVANCE ANNOUNCES CIRCUITSPACE v3.0 NEW RELEASE SUPPORTS COMPLEX HIERARCHICAL DESIGN FOR PRINTED CIRCUIT BOARDS

Pittsburgh, PA (September 10, 2008) – DesignAdvance[®] Systems, Inc., a leading developer of intelligent EDA solutions for PCB design, announces CircuitSpace[®] v3.0. The new release adds major functionality to CircuitSpace that allows the user to design and preserve physical hierarchy throughout the layout and component placement process. As designs increase in complexity, utilizing hierarchy has long been the preferred methodology for Integrated Circuits, IC packaging, schematic capture and simulation. Now CircuitSpace v3.0 brings the hierarchical methodology to the designer at the physical layout level. Designers reduce board layout and placement time from weeks-to-minutes through patented CircuitSpace technologies such as AutoClustering[™], intelligent design (IP) reuse, and Cross-Probing. CircuitSpace works seamlessly within Cadence’s Allegro PCB.

“We are impressed with the productivity gains our designers achieve with CircuitSpace.” stated Mr. Bassam Abdel-Dayem, PCB Technical Leader at Cisco Systems, Inc. “Being able to preserve the hierarchy within layout and component placement has a direct impact on time-to-market for new products, which directly correlates to profitability,” continued Mr. Abdel-Dayem.

“Since the initial release of CircuitSpace, we have worked closely with our key customers in developing the next generation intelligent EDA solutions for PCB design,” stated Mr. Edward Pupa, DesignAdvance CEO. “Bringing the hierarchical methodology to the physical layout level is a testimony to our ongoing commitment to the PCB

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designer,” continued Mr. Pupa.

CircuitSpace Featured Capabilities:

- Design with physical hierarchy throughout layout and placement
- AutoClustering™
- Bi-directional Cross-Probing between Layout and (PDF) Schematic
- Intelligent physical design reuse and replication
- Concurrent layout development project wide
- Template generation for global library usage across divisions
- Template usage with and without etch
- Automated layout reference designator propagation
- Advanced sustaining engineering and ECO process
- Automated change report between layout designs

Availability

CircuitSpace v3.0 will be available in early Q4 2008. Customers can see a demonstration of the new CircuitSpace solution and Allegro PCB at CDNLive Silicon Valley conference Sept. 9-11, and at the EMA Booth #41 at PCB West Sept. 14-19, Santa Clara, CA.

About DesignAdvance Systems, Inc.

DesignAdvance Systems Inc. develops innovative electronic design software tools that reduce the amount of time it takes to design electrical and mechanical products.

Corporate headquarters are located in Pittsburgh, Pennsylvania. For more information see www.designadvance.com

DesignAdvance is a Cadence Connections Partner.

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