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EMA TimingDesigner 9.0 – New Design Kits Save Designers Valuable Time to Market

Rochester, NY (June 4, 2007) – EMA Design Automation™, a full-service provider of Electronic Design Automation (EDA) solutions, today announced the release of TimingDesigner® 9.0 featuring Design Kits with assembled diagrams and libraries for commonly used parts and interface protocol standards, and program modifications to accommodate the latest features of the newest operating systems. “Since acquiring TimingDesigner a short time ago, our design team has been working diligently to add value to benefit our customers,” said Manny Marcano, President and CEO of EMA.

The TimingDesigner design kits are pre-assembled component timing diagrams complete with all specified libraries for speed and voltage ratings, and are intended to provide designers with a time saving head start for timing analysis of their designs. Each kit consists of all documented timing protocols associated with one or more design components, and are assembled for easy importation into TimingDesigner’s Manager Window allowing quick assembly of any timing project. These components are parameterized where applicable so that configuration options that affect timing relationships are accurately represented. Also included are the manufacturers’ data sheets, assembly notes, and any other pertinent documentation. TimingDesigner 9.0 offers Design Kits for processors from companies such as Intel and Freescale™, various memory styles such as DDR2 and Flash, and interface specifications like PCI-Express and USB.

The 9.0 release offers a new look and feel as well as improvements in performance and capabilities. Users of modern software have come to expect these new capabilities, such as configurable toolbars, standard spreadsheet operations, a modern help system, and resizable dialog boxes. In addition, TimingDesigner now allows drag and drop of TDML files and a new PDF export capability.

TimingDesigner is ideal for high-speed, multi-frequency designs where it is essential to accurately model and analyze signal relationships between devices on a board or between embedded functions on an ASIC or FPGA. It can evaluate comprehensive sets of timing alternatives and provide direction to the most complex of timing challenges, enabling designers to manage and monitor timing margins through the design process. TimingDesigner excels in using an intuitive timing diagram approach for developing specifications to drive the design process, analyzing timing to answer critical design questions, and documenting results to clearly illustrate and communicate the design implementation.

TimingDesigner 9.0 will be available July 1, 2007 at a cost of \$3,000-\$5,000 per single seat license, and is free to all customers with current maintenance contracts. For additional information, visit www.ema-eda.com or call EMA at 800-813-7494.

About EMA Design Automation, Inc.

EMA Design Automation is a full-service provider of Electronic Design Automation (EDA) solutions including a complete range of software tools, consulting services, product training, and technical support for the entire PCB and custom integrated circuit design process. EMA is a Cadence® Channel Partner serving all of North America and has been a Cadence distributor since 1998. EMA manufactures TimingDesigner, a static timing analysis solution; TimingDesigner is distributed globally through a network of worldwide representatives. EMA is a privately held corporation headquartered in Rochester, New York. Visit EMA at www.ema-eda.com for more information.

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